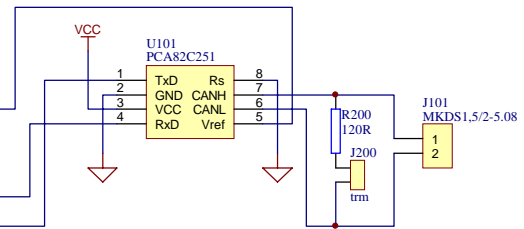


J1 TIGER A			
WAKE	1	WAKE	46 VCC
L60	2	L60	45 BATT
L61	3	L61	44 AGND
L62	4	L62	43 VREF
L63	5	L63	42 AN3
L64	6	L64	41 AN2
L65	7	L65	40 AN1
L66	8	L66	39 AN0
L67	9	L67	38 ALARM
L70	10	L70	37 PC
L71	11	L71	36 L40
L72	12	L72(PWM)	35 L42
L73	13	L73(PWM)	34 L37
L80	14	L80	33 L36
L81	15	L81	32 L35
L82	16	L82	31 L34
L83	17	L83	30 L33
L84	18	L84	29 L95
L85	19	L85	28 L94
L86	20	L86	27 L93
L87	21	L87	26 L92
RESE	22	Reset	25 L91
GND	23	GND	24 L90

J2 TCAN			
WAKE	1a	WAKE	46b VCC
L60	2a	L60	45b BATT
L61	3a	L61	44b AGND
L62	4a	L62	43b VREF
L63	5a	L63	42b AN3
L64	6a	L64	41b AN2
L65	7a	L65	40b AN1
L66	8a	L66	39b AN0
L67	9a	L67	38b ALARM
L70	10a	L70	37b PC
L71	11a	L71	36b L40
L72	12a	L72(PWM)	35b L42
L73	13a	L73(PWM)	34b L37
L80	14a	L80	33b L36
L81	15a	L81	32b L35
L82	16a	L82	31b L34
L83	17a	L83	30b L33
L84	18a	L84	29b L95
L85	19a	L85	28b L94
L86	20a	L86	27b L93
L87	21a	L87	26b L92
RESE	22a	Reset	25b L91
GND	23a	GND	24b L90



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